### **DECLARATION OF JOINT INVENTORS FOR PATENT APPLICATION**

As the below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below next to my name.

I believe I am the original, first and joint inventor of the subject matter which is claimed and for which a patent is sought on the invention entitled: "Methods of Forming Memory Circuitry", the specification of which is attached hereto.

I hereby state that I have reviewed and understand the contents of the above-identified specification, including the claims.

I acknowledge the duty to disclose information known to me to be material to patentability as defined in Title 37, Code of Federal Regulations § 1.56.

#### PRIOR FOREIGN APPLICATIONS:

I hereby state that no applications for foreign patents or inventor's certificates have been filed prior to the date of execution of this declaration.

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statement may jeopardize the validity of the application or any patent issued therefrom.

Full name of inventor: Kunal R. Parekh Inventor's Signature: Date: Residence: Boise, Idaho **United States of America** Citizenship: Post Office Address: 677 North Morningside Way, Boise, ID 83712 Full name of inventor: Byron N. Burgess Inventor's Signature: Date: Boise, Idaho Residence: Citizenship: **United States of America** 

13543 West Engelmann Drive, Boise, ID 83713

Post Office Address:

### IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No	Inknown
Filing Date Filed	Herewith
inventor Kunal B. Pare	kh et al
Assignee Micron Technol	oay Inc
Group Art Unit	Inknown
Examiner	Inknown
Attorney's Docket No	22-2260
Title: Methods of Forming Memory Circuitry	22 2200

# POWER OF ATTORNEY BY ASSIGNEE AND CERTIFICATE BY ASSIGNEE UNDER 37 CFR § 3.73(b)

To: Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

Sir:

MICRON TECHNOLOGY, INC., the Assignee of the entire right, title and interest in the above-identified patent application by assignment attached hereto, hereby appoints the attorneys and agents of the firm of WELLS ST. JOHN P.S., listed as follows:

David P. Roberts	Reg.	No.	23,032
Randy A. Gregory	Reg.	No.	30,386
Mark S. Matkin	Reg.	No.	32,268
Deepak Malhotra	Reg.	No.	33,560
Mark W. Hendricksen	Reg.	No.	32,356
David G. Latwesen	Reg.	No.	38,533
George G. Grigel	Reg.	No.	31,166
Keith D. Grzelak	Reg.	No.	37,144
James D. Shaurette	Reg.	No.	39,833
James E. Lake	Reg.	No.	44,854
D. Brent Kenady	Reg.	No.	40,045
Jennifer J. Taylor	Reg.	No.	48,711
Robert C. Hyta	Reg.	No.	46,791
Satheesh K. Karra	Reg.	No.	40,246

and also attorneys Michael L. Lynch (Reg. No. 30,871) and Charles B. Brantley II (Reg. No. 38,086) of Micron Technology, Inc., as its attorneys with

full power of substitution to prosecute this application and transact all business in the Patent and Trademark Office connected therewith.

The Assignee certifies that the above-identified Assignment has been reviewed and to the best of Assignee's knowledge and belief, title is in the Assignee, and a copy of the Assignment is submitted herewith.

Please direct all correspondence regarding this application to:

Customer No. 021567 Wells St. John P.S. Attn: Mark S. Matkin 601 West First Avenue, Suite 1300 Spokane, WA 99201-3828

Telephone: (509) 624-4276 Facsimile: (509) 838-3424

MICRON TECHNOLOGY, INC.

Dated: 7-1-03 By: AV

Name: Michael L. Lynch, Esq.

Title: Chief Patent Counsel

Attachments: Copy of Assignment, Copy of Board of Directors' Resolution

#### **ASSIGNMENT**

#### PARTIES TO THE ASSIGNMENT:

#### **INVENTORS:**

Kunal R. Parekh Byron N. Burgess COPY

#### **ASSIGNEE:**

Micron Technology, Inc. Corporation of the State of Delaware 8000 South Federal Way Boise, Idaho 83716

#### **BACKGROUND OF THIS ASSIGNMENT:**

Inventors have conceived certain new and useful inventions disclosed in a United States patent application titled "Methods of Forming Memory Circuitry".

#### THE PARTIES AGREE AS FOLLOWS:

In consideration of good and valuable consideration, the receipt, sufficiency and adequacy of which is hereby acknowledged, INVENTORS have sold, assigned and transferred, and by these presents do hereby sell, assign and transfer to ASSIGNEE the entire right, title and interest in the above-identified inventions and application and to any reissues, renewals, divisions or continuations of the application thereof, and hereby authorizes the Commissioner of Patents and Trademarks to issue such Letters Patent to ASSIGNEE for the sole use of ASSIGNEE, its successors or assigns.

INVENTORS further agree to execute, at the request and expense of ASSIGNEE such other formal documents as may be required to fully convey the interest transferred herein and will similarly execute any application papers required for the filing of any division, continuation, renewal or reissue of the

## COPY

patent application or resulting Letters Patent; and will generally do everything necessary or desirable to obtain and enforce proper protection for the inventions assigned hereby.

INVENTORS further assign to ASSIGNEE the whole right, title and interest in the inventions disclosed in the application throughout all countries foreign to the United States. ASSIGNEE is hereby authorized to apply for patents relating to the inventions in its own name in countries where such procedure is proper; to claim the benefit of the International Convention; to file and prosecute International Applications relating to the inventions under the Patent Cooperation Treaty; and to file and prosecute applications relating to the inventions under the European Patent Convention. INVENTORS agree to execute applications relating to the inventions in those countries and under those conventions where it is necessary that the same be executed by the inventor, and to execute assignments of such applications and the resulting Letters Patent to ASSIGNEE as well as all other necessary papers in relation to such applications and the resulting Letters Patent to ASSIGNEE as well as all other necessary papers in relation to such applications and Letters Patent. INVENTORS hereby consent that a copy of this assignment shall be deemed a full legal and formal equivalent of any document which may be required in any country as proof of the right of ASSIGNEE to apply for patent and other form of protection for said inventions and to claim the aforesaid benefit of the right of priority.

INVENTORS further warrant and covenant that no assignment, grant, mortgage, license or other agreement affecting the rights and property herein conveyed has been or will be made to others by the undersigned.

To be binding on the heirs, assigns, representatives and successors of the undersigned and extend to the successors, assigns and nominees of the Assignee.

Dated:	6/35/2003	Signature:	Kunal R. Parek	Theel
State of $\sqrt{}$	^ -	) ) ss. )	COPY	1
appeared t described acknowledg	ORE ME, thisthe above-named in in and who executed to me that he/she therein expressed.	ventor, known to uted the foregoine executed the s	me to be the ng assignment same of his/her of	person who is instrument and
Data di <i>l</i>	OF OF OF OBLICATION OF OBLICAT	My Commi	Consular Officer ssion Expires:	5/18/01
State of		Signature:	By on N. Burge	ess
County of	^	) ) ss. )		
appeared described acknowledg	ORE ME, this	inventor, known t uted the foregoi he executed the	o me to be the ng assignment	instrument and
SEAL	STARA SKELLO		<i>Queba</i> Consular Officer ssion Expires: _	1 5/18/01

## MICRON TECHNOLOGY, INC. CERTIFIED COPY OF RESOLUTIONS

I, Jan R. Reimer, the Assistant Secretary of Micron Technology, Inc. do hereby certify, that the resolutions attached hereto represent a complete, true and correct copy of the resolutions duly adopted by the Board of Directors of Micron Technology, Inc., a corporation duly organized and existing under the laws of the State of Delaware, at a meeting duly held on March 25, 1996, a quorum being present, and have been entered into the minutes of said meeting; that I am the keeper of the corporate seal and of the minutes and records of this Corporation; and that the said resolutions have not been rescinded or modified.

The resolutions attached hereto are in conformity with the Articles of Incorporation and Bylaws of the Corporation and are now in full force and effect.

I further certify that the person whose name and signature is set out below is the person authorized to act for said corporation in transactions with and pursuant to the foregoing resolutions, and that such person is now duly qualified and acting in his respective capacity:

NAME AND TITLE

SIGNATURE

Michael L. Lynch, Assistant General Counsel for Intellectual Property

IN WITNESS WHEREOF, I have hereunto subscribed my name and affixed the seal of the said corporation, this \_bth\_ day of \_\_\_\_\_\_\_\_\_, 1996.

an R. Reimer, Assistant Secretary

(SEAL)

# MICRON TECHNOLOGY, INC. BOARD OF DIRECTORS RESOLUTIONS

WHEREAS, certain key employees require the authority to execute certain documents on behalf of the Company in order to enable them to effectively and efficiently carry out their responsibilities and duties to the Company.

NOW THEREFORE BE IT RESOLVED, that the Board hereby approves and authorizes Mr. Michael L. Lynch, Assistant General Counsel for Intellectual Property, to execute on behalf of the Company, documents pertaining to the Company's patent prosecution matters, including but not limited to documents relating to representation before a patent examining authority, patent terms and other patent prosecution procedures, both in the United States and other countries, upon such terms and conditions as the General Counsel of the Company shall deem necessary or appropriate.